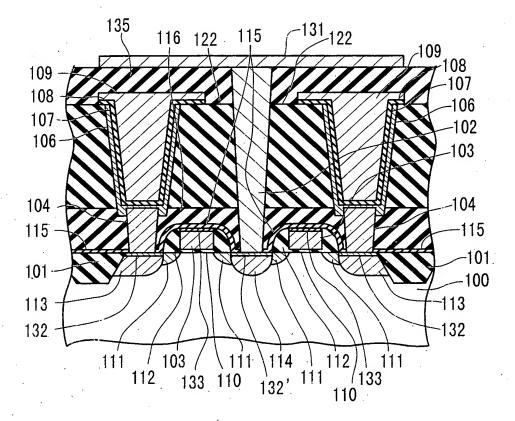
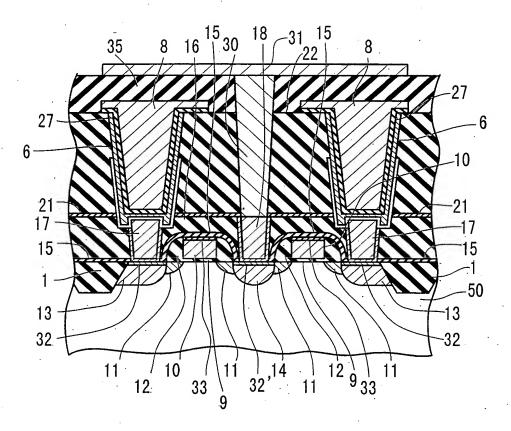


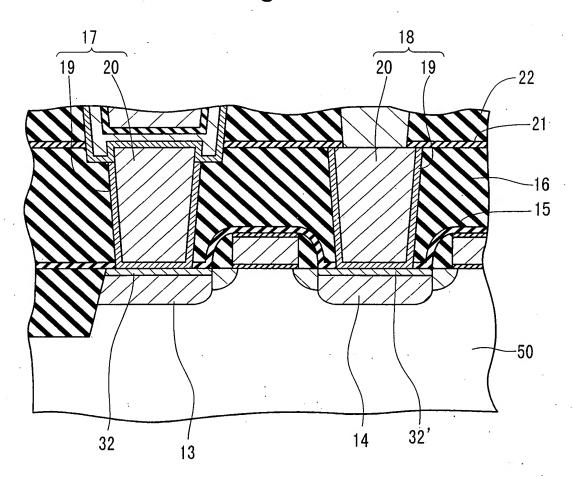
## Fig. 1 PRIOR ART



F i g . 2



F i g . 3





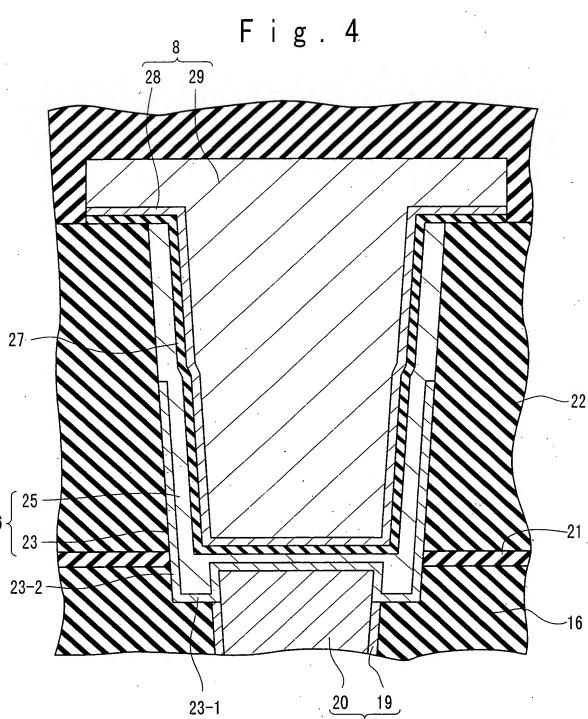




Fig. 5

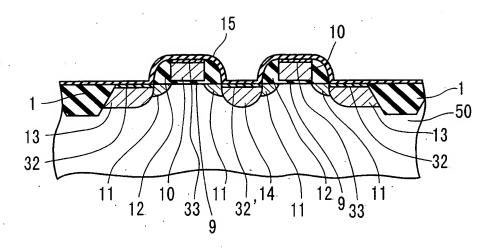
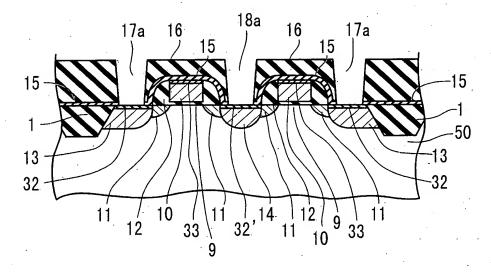


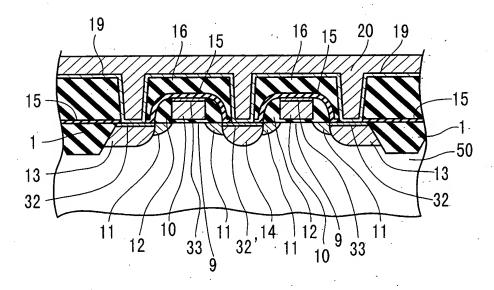


Fig. 6





F i g . 7





F i g . 8

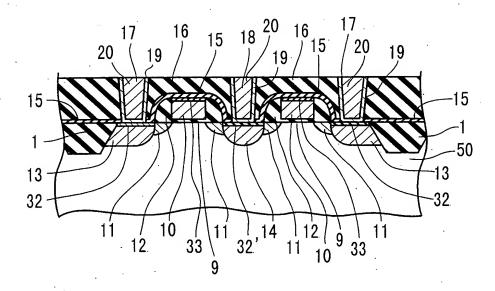




Fig. 9

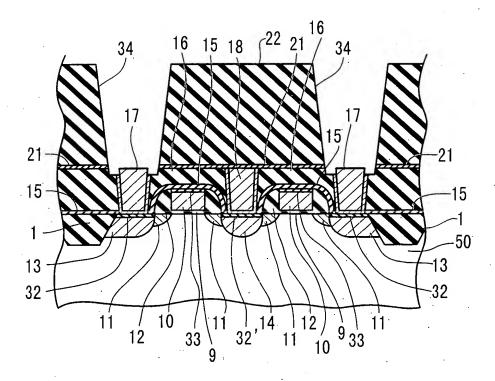




Fig. 10

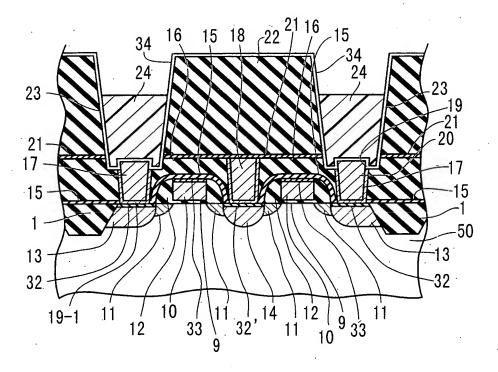




Fig. 11

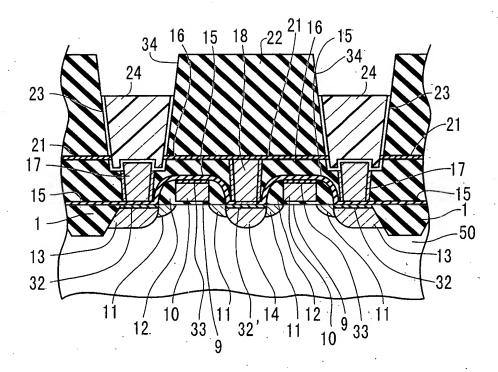


Fig. 12

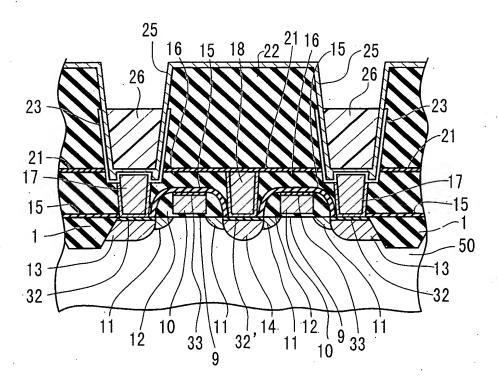


Fig. 13

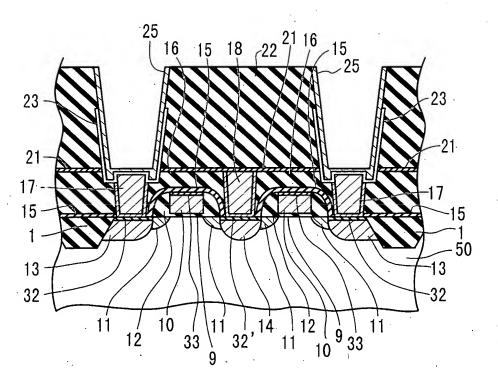


Fig. 14

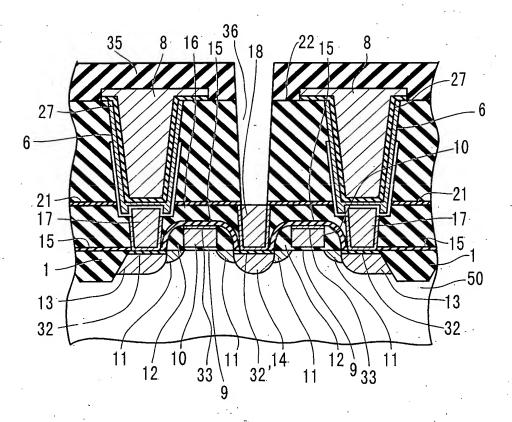
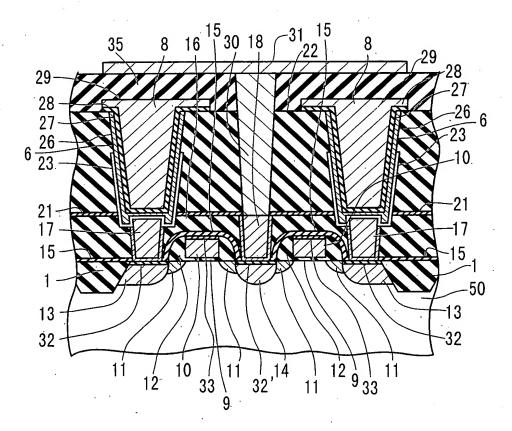
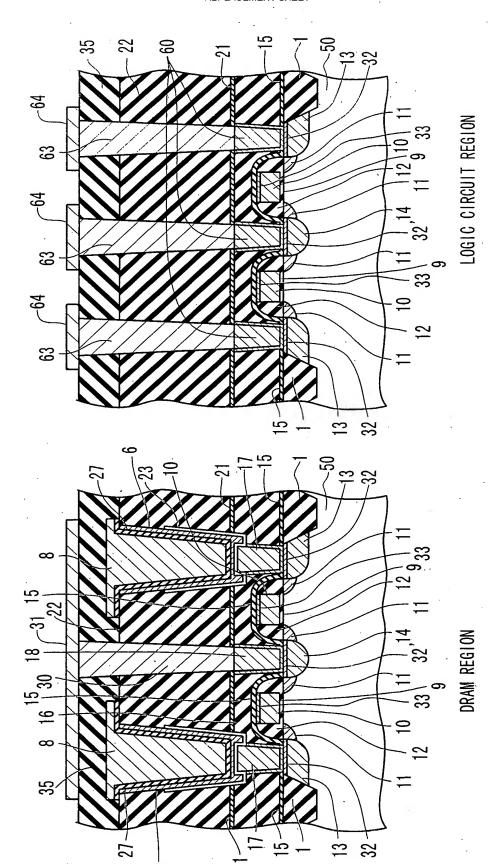


Fig. 15







<u>6</u> 

<u>∞</u>

LOGIC CIRCUIT REGION

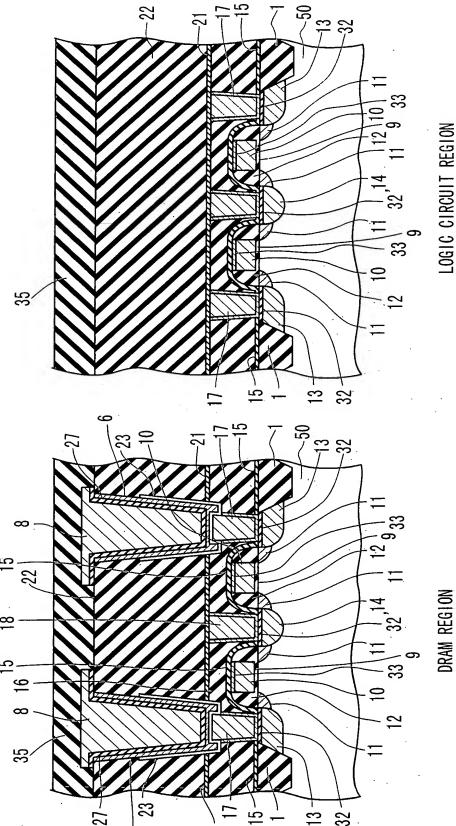
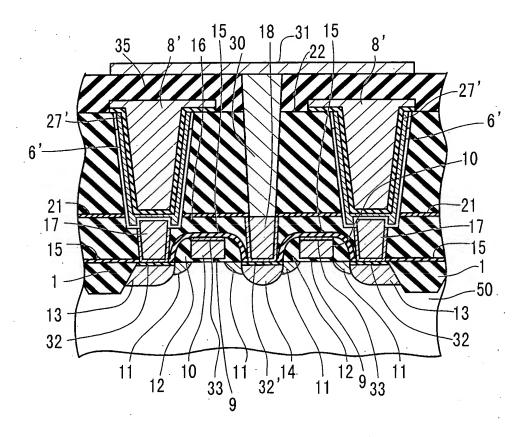


Fig. 19



SALEN S TRADEN

DOCKET NO.: 8022-1003 APPLN NO.: 10/068,893 REPLY TO NOTICE OF ALLOWABILITY OF: MAY 25, 2004 REPLACEMENT SHEET

Fig. 20

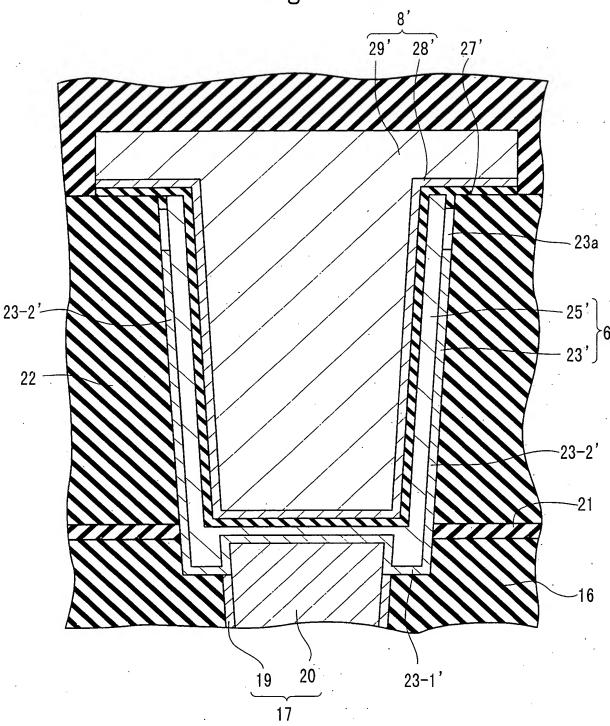
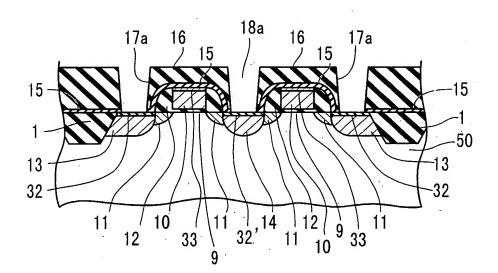




Fig. 21



F i g . 22

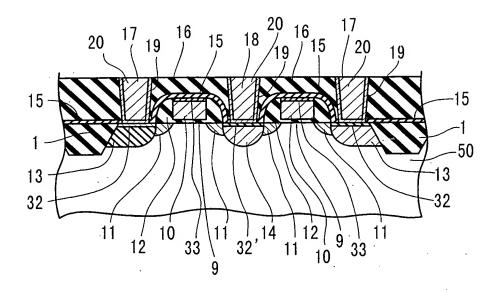


Fig. 23

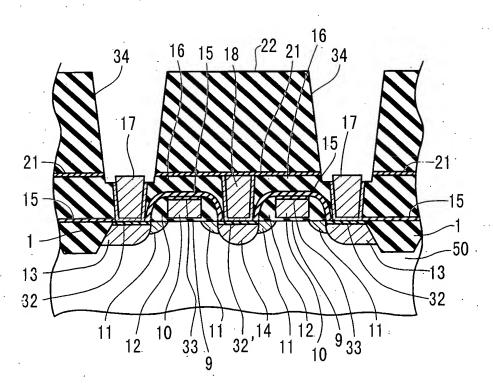
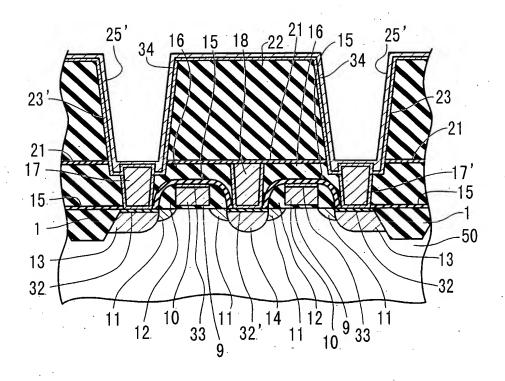


Fig. 24



## Fig. 25

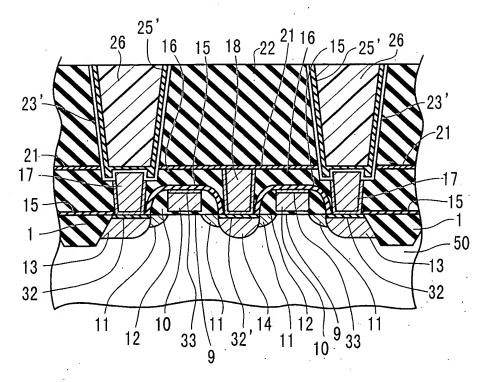


Fig. 26

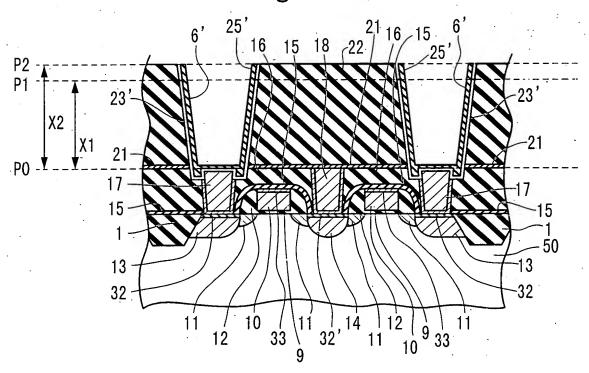
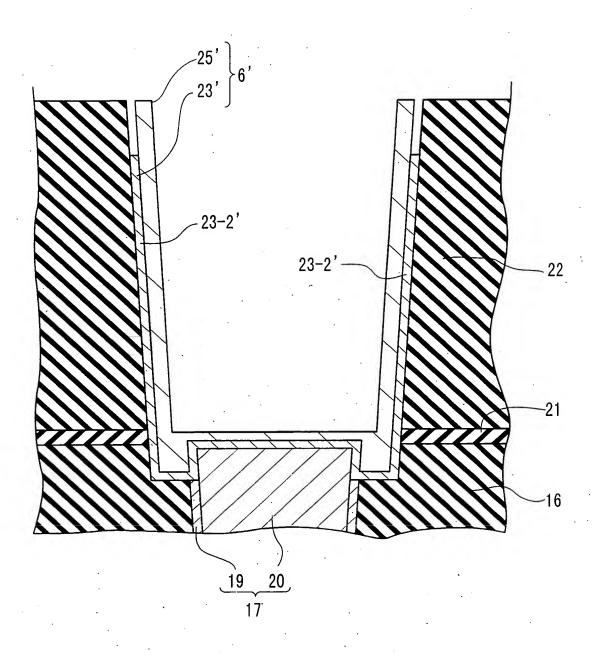


Fig. 27



F i g . 28

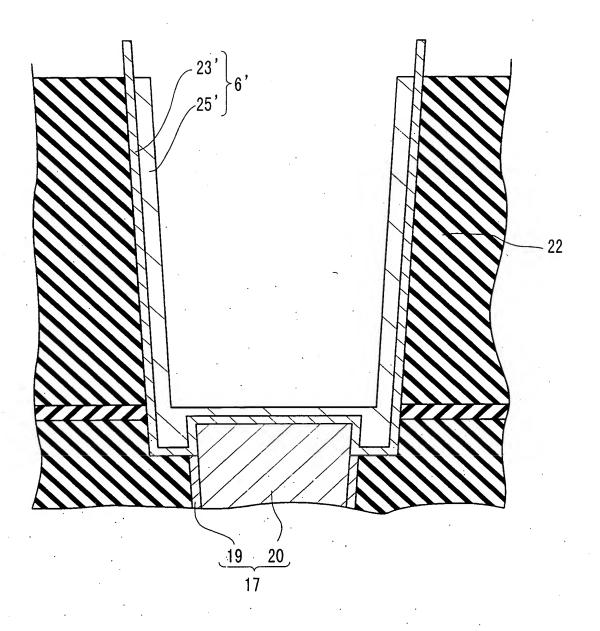


Fig. 29

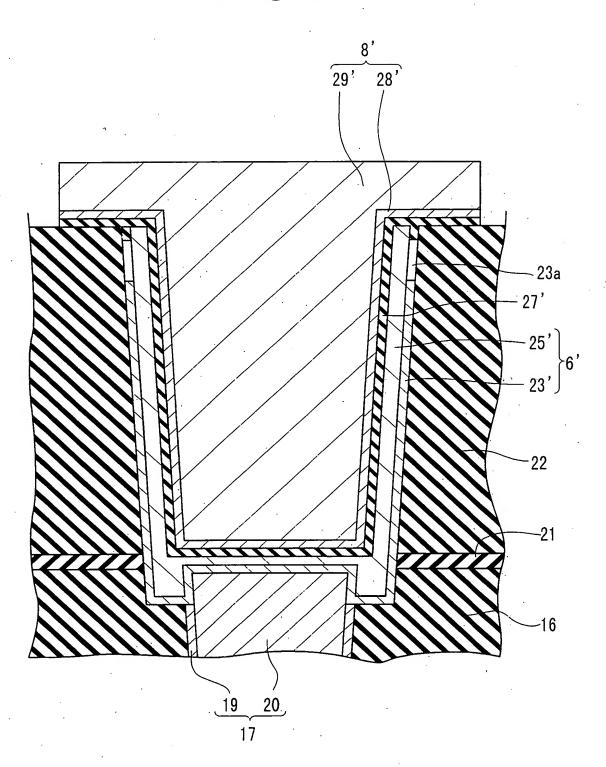


Fig. 30

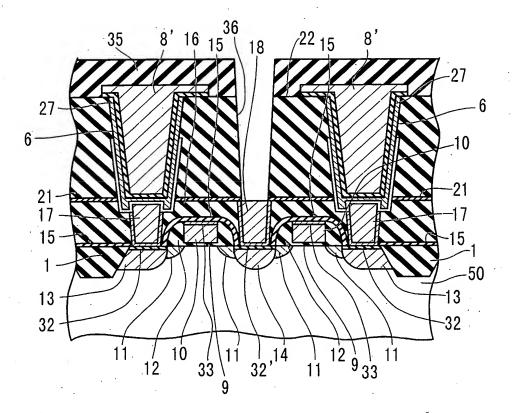


Fig. 31

